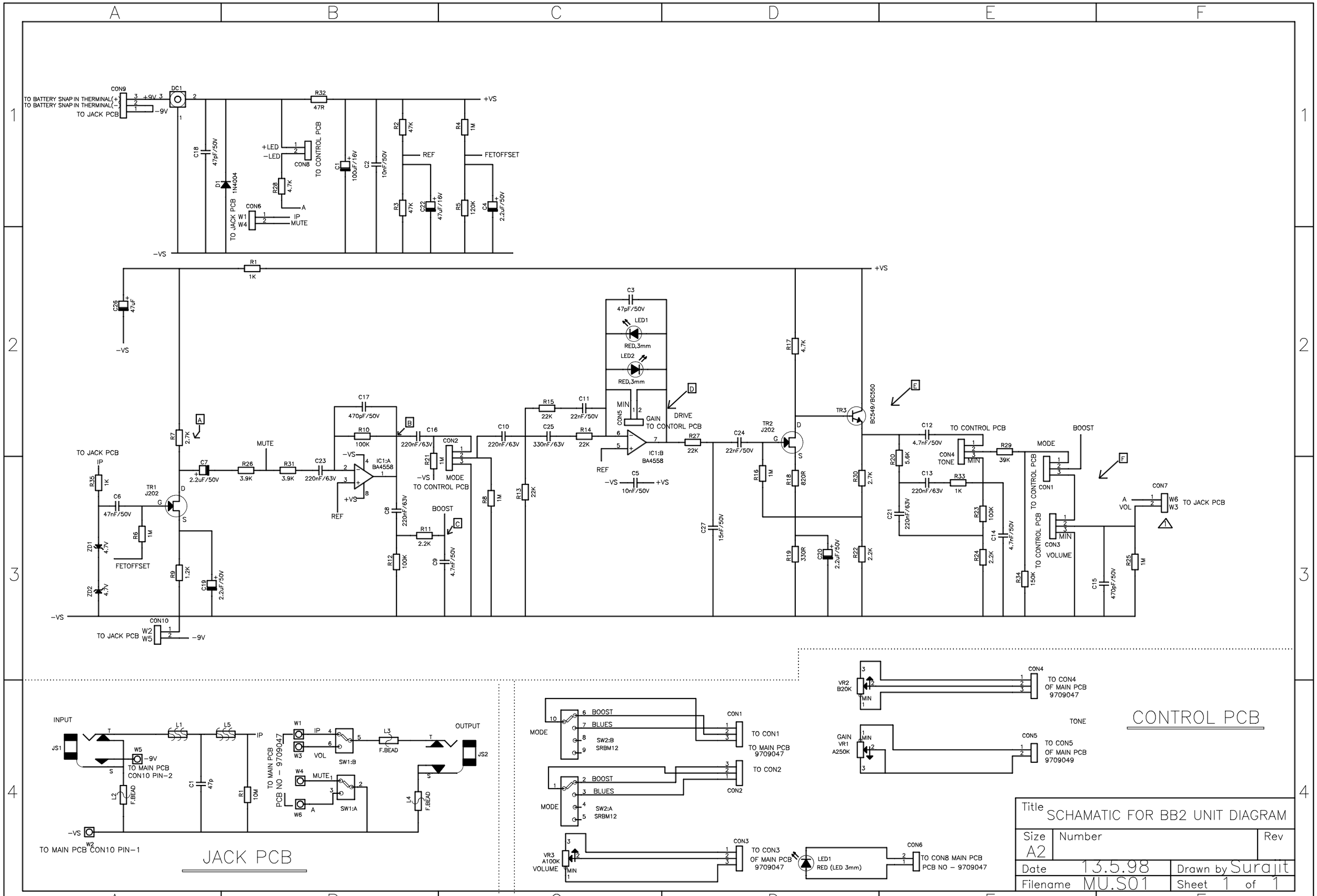
CON9
TO BATTERY SNAP IN THERMINAL(+)
TO BATTERY SNAP IN THERMINAL(-)
TO JACK PCB
+9V
-9V
DC1
R32
47R
+VS
-VS
C18
47pF/50V
D1
1N4004
TO JACK PCB
+LED
-LED
CON8
TO CONTROL PCB
R28
4.7K
CON6
W1
W4
IP
MUTE
A
C1
100uF/16V
C2
10nF/50V
R2
47K
R3
47K
REF
C22
47uF/16V
R4
1M
R5
120K
FETOFFSET
C4
2.2uF/50V
R1
1K
C26
47uF
C3
47pF/50V
LED1
RED,3mm
LED2
RED,3mm
R17
4.7K
TR3
BC549/BC550
E
D
MIN
CON5
GAIN
TO CONTORL PCB
DRIVE
IC1:B
BA4558
C5
10nF/50V
R27
22K
C24
22nF/50V
TR2
J202
R16
1M
R18
820R
R19
330R
C20
2.2uF/50V
C27
15nF/50V
R22
2.2K
R30
2.7K
C12
4.7nF/50V
TO CONTROL PCB
CON4
TONE
R29
39K
MODE
BOOST
R20
5.6K
C13
220nF/63V
R33
1K
C21
220nF/63V
R23
100K
R24
2.2K
C14
4.7nF/50V
R34
150K
CON1
CON3
VOLUME
F
CON7
A
VOL
W6
W3
TO JACK PCB
R25
1M
C15
470pF/50V
R7
2.7K
A
TO JACK PCB
IP
R35
1K
C6
47nF/50V
TR1
J202
R6
1M
ZD1
4.7V
ZD2
4.7V
FETOFFSET
R9
1.2K
C19
2.2uF/50V
CON10
W2
W5
-9V
C7
2.2uF/50V
R26
3.9K
MUTE
R31
3.9K
C23
220nF/63V
C17
470pF/50V
R10
100K
IC1:A
BA4558
REF
B
C16
220nF/63V
R21
1M
MODE
C8
220nF/63V
R11
2.2K
BOOST
C
R12
100K
C9
4.7nF/50V
CON2
C10
220nF/63V
R8
1M
R13
22K
R15
22K
C11
22nF/50V
C25
330nF/63V
R14
22K
INPUT
JS1
W5
-9V
TO MAIN PCB
CON10 PIN-2
L1
L5
IP
L2
F.BEAD
C1
47p
R1
10M
W1
W3
VOL
SW1:B
W4
MUTE
W6
A
SW1:A
TO MAIN PCB
PCB NO - 9709047
L3
F.BEAD
L4
F.BEAD
OUTPUT
JS2
W2
TO MAIN PCB CON10 PIN-1
JACK PCB
MODE
BOOST
BLUES
SW2:B
SRBM12
CON1
TO CON1
TO MAIN PCB
9709047
TO CON2
CON2
SW2:A
SRBM12
VR3
A100K
VOLUME
CON3
TO CON3
OF MAIN PCB
9709047
LED1
RED (LED 3mm)
CON6
TO CON8 MAIN PCB
PCB NO - 9709047
VR2
B20K
CON4
TO CON4
OF MAIN PCB
9709047
TONE
GAIN
VR1
A250K
CON5
TO CON5
OF MAIN PCB
9709049
CONTROL PCB
Title
SCHAMATIC FOR BB2 UNIT DIAGRAM
Size
A2
Number
Rev
Date
13.5.98
Drawn by
Surajit
Filename
MU.S01
Sheet
1
of
1